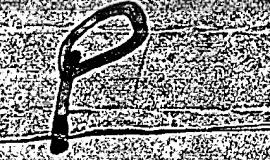
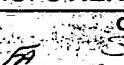


JC986 U.S. PTO 09/902483		PATENT NUMBER 07/11/01	
Class:	Subclass:	ISSUE CLASSIFICATION	
			
U.S. UTILITY Patent Application			
<i>SC-852</i>  O.I.P.E. SCANNED <i>AC3</i> Q.A. <i>JH</i>		PATENT DATE <i>10/01/01</i>	
APPLICATION NO. 09/902483	CONT/PRIOR D	CLASS 438	SUBCLASS <i>602</i>
ART UNIT 2813	EXAMINER <i>KIELIN</i>		
APPLICANTS Cyril Cabral Kevin Chan Guy Cohen Christian Lavoie			
TITLE Self-aligned silicide (salicide) process for low resistivity contacts to thin film silicon-on-insulator and bulk MOSFETs and for shallow Junctions			
PTO-2040 12/99			

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED	
		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to (date) has been disclaimed.					NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____.					ISSUE FEE	
		(Primary Examiner)		(Date)	Amount Due	Date Paid
<input checked="" type="checkbox"/> The terminal -- months of this patent have been disclaimed.		(Legal Instruments Examiner)		(Date)	ISSUE BATCH NUMBER	

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